

PATENT  
Customer No. 22,852  
Attorney Docket No. 04329.2217

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of: )  
)  
Masue SHIBA et al. ) Group Art Unit: 2134  
)  
Application No.: 09/487,483 ) Examiner: M. Simitoski  
)  
Filed: January 19, 2000 )  
)  
For: ARITHMETIC METHOD AND )  
APPARATUS AND CRYPTO )  
PROCESSING APPARATUS FOR )  
PERFORMING MULTIPLE TYPES )  
OF CRYPTOGRAPHY (As )  
amended)

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

**INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97(c)**

Pursuant to 37 C.F.R. §§ 1.56 and 1.97(c), Applicants bring to the attention of the Examiner the documents listed on the attached PTO-SB-08 form. Applicants file this Information Disclosure Statement (IDS) after the period set forth in 37 C.F.R. § 1.97(b), but before the mailing date of either a Final action, Quayle action, or a Notice of Allowance, to the undersigned representative's knowledge at the time of filing this IDS.

The documents listed in this IDS were first cited in a communication from the French Patent Office in a counterpart French application, and this IDS is being filed within three months of the mailing date of that communication. A copy of French Search Report is attached.

Applicants attach copies of the listed documents. Applicants respectfully request that the Examiner consider the listed documents and indicate that they were considered by making appropriate notations on the attached form.

This submission does not represent that a search has been made or that no better art exists and does not constitute an admission that each or all of the listed documents are material or constitute "prior art." If the Examiner applies any of the documents as prior art against any claims in the application and Applicants determine that the cited documents do not constitute "prior art" under United States law, Applicants reserve the right to present to the office the relevant facts and law regarding the appropriate status of such documents.


Applicants further reserve the right to take appropriate action to establish the patentability of the disclosed invention over the listed documents, should one or more of the documents be applied against the claims of the present application.

If there is any fee due in connection with the filing of this Statement, please charge the fee to our Deposit Account No. 06-0916.

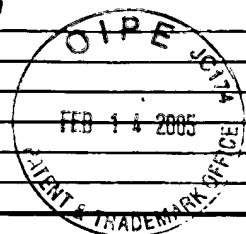
Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,  
GARRETT & DUNNER, L.L.P.

Dated: February 14, 2005

By:   
Wenye Tan  
Reg. No. 55,662

IDS Form PTO/SB/08: Substitute for form 1449A/PTO			<b>Complete if Known</b>		
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  <i>(Use as many sheets as necessary)</i>			Application Number	09/487,483	
			Filing Date	January 19, 2000	
			First Named Inventor	Masue SHIBA	
			Art Unit	2134	
			Examiner Name	M. Simitoski	
			Attorney Docket Number	04329.2217	
Sheet	1	of	1		



U.S. PATENTS AND PUBLISHED U.S. PATENT APPLICATIONS					
Examiner Initials	Cite No. <sup>1</sup>	Document Number	Issue or Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			

**Note: Copies of the U.S. Patent Documents are not Required in IDS filed after October 21, 2004**

FOREIGN PATENT DOCUMENTS						
Examiner Initials	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	Translation <sup>6</sup>
		Country Code <sup>3</sup> Number <sup>4</sup> Kind Code <sup>5</sup> (if known)				
		DE-196 44 688 A1	04-30-1998			

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	Translation <sup>6</sup>
		W. Drescher et al., VLSI Architectures for Multiplication in GF(2 <sup>m</sup> ) for Application Tailored Digital Signal Processors, VLSI Signal Processing, IX, 1996., WORKSHOP ON SAN FRANCISCO, CA, USA 10/30 – 11/1/1996. ISBN: 0-7803-3134-6.	
		Search Report issued by French Patent Office on 12-16-04 in French Application No. 0000680	

Examiner Signature		Date Considered	
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.